AMENDMENTS TO THE SPECIFICATION

Amend paragraph beginning Page 11, line 24 of the specification as follows:

In step S14, in Fig. 4, the CPU 10 scales up/down the image Ty/Iy-fold, where Ty is a

height of the text while Iy is a height of width of the image. If the image is a vector image, the

processing of scaling up/down is implemented by a coordinate transformation. If the image is a

bitmapped image, the processing of scaling up/down is implemented by a coordinate

transformation and interpolation processing. Since the processing of scaling up/down the image

is a known technique, the detailed description thereof will not be repeated.

MRC/MH/das/src